

	Class	Subclass
ISSUE CLASSIFICATION		

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED NS 4 Q.A.

APPLICATION NO. 09/655893	CONT/PRIOR	CLASS 961 174	SUBCLASS 2.55	ART UNIT 2841 2867	EXAMINER 2841 JIA
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APPLICANTS

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TITLE

Electrical circuit board and a method for making the same

PTO-2040
12/99

ISSUING CLASSIFICATION							
ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
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Amount Due			Date Paid	
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